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Dr. Usha Sree, Director IQAC JBIET Hyderabad.
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Dr. K. Prakash, Principal, Vaagdevi College of Engineering Warangal.
Dr. P. Prasad Rao, Professor, Vaagdevi Engineering College Warangal.
Prof. K. Laxmi Narayana, Chairman membership and Centers Committee IETE, New Delhi.
Prof. M. Sreelatha, Chair Person IETE WGL Sub-Center.
Mr. G. Raghobham Reddy, Hon. Secretary IETE WGL Sub-Center.



Conference Venue

SR Engineering College, Ananthasagar, Hasanparthy, Warangal, Telangana, INDIA-506371

Accommodation

On prior intimation, accomodation will be arranged in hotels at Warangal city. Hotel tariff ranges from 1500 (\$25) to 4200 (\$70). Transport will be provided from hotels to the venue of the conference, which is at a distance of 15 km.

Organizing Committee

Chief Patron : Sri A. Varada Reddy , Chairman, SR Educational Society
Patron : Dr. P. Venkateswarlu , Director, SR Engineering College, Warangal
Conference Chair : Dr. C.V. Guru Rao , Principal, SR Engineering College, Warangal
Organizing Secretary : Dr. T. Anil Kumar, Professor, Dept. of Electronics & Comm. Engineering
Convenor(s) : Dr. Syed Musthak Ahmed, Head, Dept. of Electronics & Comm. Engineering
Mr. M. Sampath Reddy, Assoc. Professor, Dept. of Electronics & Comm. Engineering

Members

Dr. V. Mahesh, Dean (Research), SREC
Dr. R. Archana Reddy, Head, Dept. of Humanities & Sciences
Dr. G.V. Praveen, Head, Dept. of Civil Engineering
Dr. P. Sammaiah, Head, Dept. of Mechanical Engineering
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Mr. N. Govardhan, Asst. Professor, Dept. of Electronics & Comm. Engineering

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SR ENGINEERING COLLEGE

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Autonomous



INTERNATIONAL CONFERENCE ON
"RECENT ADVANCES IN COMMUNICATION, VLSI & EMBEDDED SYSTEMS"

(19th & 20th December, 2014)

Organised by Department of
Electronics & Communicaton Engineering.

Sponsored by
Department of Science and Technology
New Delhi.

About the Conference

The aim of the conference is to bring together scientists, researchers, educationalists and developers to share new ideas and establish cooperation between them and provide a platform for presenting their original work and exchange ideas, information, techniques and applications in the field of Communication, VLSI & Embedded Systems. Participants are encouraged to present papers on all topics listed in the CALL FOR PAPERS, which enable interdisciplinary discussions on new ideas, latest research and development. Papers are invited from various departments like Aeronautical, Automobile, CSE, ECE, EEE, EIE, Inst. & Control, Mechatronics, and allied branches of Engineering. Selected Papers will be published in the Proceedings of the conference.

Objectives of the conference

- Communication and signal processing have led to unprecedented increase in the number of engineering applications such as Industrial Automation, Computer Communications, Satellite Communications, and Vehicle Control etc. The conference focuses on the recent trends advancement and future trends in the areas of communications, signal processing, VLSI, wireless and mobile communications. The areas of VLSI, Embedded systems and Signal Processing have improved the quality of human life immensely. Innovations in recent years in the areas of mobile computing, biomedical processing, multimedia entertainment devices etc. have contributed towards the same
- A major objective of the conference will be to pursue the progression from the theoretical aspects of innovative communication and signal processing techniques through the implementation, evaluation and performance enhancement of practical communication systems. It is also planned to provide a forum for presenting the latest ideas from academia, industry and the corresponding developments in the domains of signal processing and integrated circuits and systems.
- This Conference aims at bringing together the Researchers, Scientists, Engineers and Scholar students in all areas of VLSI, Communication and provides an international forum for the dissemination of original research results, new ideas and practical development experiences which concentrate on both theory and practices.
- The Conference covers wide range of topics from Communication, VLSI circuits, systems and design methods to system level design and system-on-chip issues in bringing VLSI experience to new areas and technologies like nano and molecular devices, MEMS, Communication and network security. Future design methodologies will also be one of the key topics at the conference.
- Yet another major goal of the conference is to promote academic-industry interaction and foster collaborative research. Apart from these, we expect papers dealing with data, audio, image and video processing for multimedia, biomedical and security applications.

Scope and Topics

Conference Invites original & Unpublished Reserch Papers in the following areas

- Antennas
- Embedded Systems
- VLSI Design
- Audio & Speech Signal Processing
- Wireless Communications
- Bio Medical Signal Processing
- MEMS
- Data communications
- Mixed Signal Processing
- Optical communication & networks
- Digital System Design
- Image & Video Signal Processing
- Neural networks & Fuzzy Logic Applications
- Adaptive Signal Processing
- NEMS

These are suggested areas papers are accepted in related areas also.

Submission of Papers

Authors are invited to submit only full length papers not exceeding 4 pages as per IEEE format which is available on the Conference website. Submission of papers shall be through email: icracves@srecwarangal.ac.in on or before the deadline.

Information about the guidelines to authors, paper format and copyright format are provided on conference website: www.srecwarangal.ac.in/icracves.html Authors and the participants are requested to watch the Conference website for the updates.

Acceptance and Publication

All the papers submitted will be peer reviewed by two experts in a double blinded manner and based on the recommendations of the reviewers, comments will be sent to the authors. The camera-ready manuscripts after incorporating the reviewer comments should be submitted before the deadline. The papers accepted for presentation will be published in the proceedings of the conference and the softcopy of the proceedings will be presented in the form of CD. Papers submitted after deadline may not be included in the conference proceedings.

Keynote Speakers

Dr. V. U. Reddy, Honorary professor, IIT, Hyderabad.
Dr. K. Ashoka Reddy, Professor, KITS, Warangal.
Dr. N. V. S. N. Sarma, Professor of ECE, NIT, Warangal.
Dr. P. Chandrasher, Dept of ECE, OU, Hyderabad.
Dr. S. Ravi, Professor & Head, DR.MGR Educational & Research University, Chennai.
Dr. L. Pratap Reddy, Professor of ECE, JNTUHCEH.
Dr. Hemalatha Rallapalli, OU, Hyderabad.
Dr. T. Srinivas, Associate Professor, IISc, Bangalore.
Dr. Usha Sree, Director (IQAC), JBIET, Hyderabad.

Important Dates

Submission of full length paper : 08th November 2014
Acceptance of the paper : 27th November 2014
Camera Ready Paper Submission : 08th December 2014
Last date for Registration : 08th December 2014

Registration Fee

Delegates	National	International
Students & Full-Time Research Scholars	₹ 2000	\$ 125
Delegates from Academia & Research	₹ 2500	\$ 150
Delegates from Industry	₹ 3000	\$ 175

* Includes Publication Charges, Conference Kit, Refreshments and Lunch during Conference days. Accommodation is not included.

International Advisory Committee

Dr. Andrzej Rucinski, Professor, University of Hampshire
Dr. Santosh Pande, Professor, College of Computing, Georgia Institute of Technology, Atlanta.
Dr. Ada Gavrilovska, Research Scientist, Georgia Institute of Technology, Atlanta.
Dr. S K RAMESH, Dean, CSUN, California State University, North Ridge.
Dr. Alphonse Arokiasamy, Professor, NTU, Electrical Engineering School.



About the Institution

SR Engineering College, Warangal founded in 2002, is an autonomous and accredited Institution valuing and encouraging creativity and quality in teaching and learning processes for the intellectual growth of its students. The staff and students take on new and interesting activities to acquire ability to think uniquely and independently. The college is in a position to attract and develop outstanding faculty to actively participate and support an open academic climate in the campus. The college adopts innovative approaches for continuous improvement by strategic planning, benchmarking and performance monitoring.

SR Engineering College (SREC) is currently handling number of funded research projects creating research avenues for the faculty and students. Through active industry cooperation, SREC has established centers like Microsoft Innovation Center, CISCO Networking Academy and IBM Center of Excellence for nurturing talent among students. To shape and transform the students to meet challenging and complex engineering tasks globally, the college has systematically built and fostered relationship with reputed US Universities like University of Massachusetts, Saint Louis University, University of Missouri and Wright State University.

About the Department

- The department of ECE was established in the year 2002. The department has well qualified faculties and the state of the art labs to run both UG & PG programs. The department has one UG Program with an intake of 180 and two PG programs - Embedded systems with an intake of 36 and Electronic design technology with an intake of 18.
- Most of the faculty are pursuing their Doctorate besides executing research projects with grants from UGC, DST, AICTE, etc. The students and Staff of ECE are actively involved in publishing papers in various conferences and journals.
- The faculties are encouraged to take up research, attend workshops and conferences. Several faculty development programs are also organized for the benefits of the faculty.
- To cater the needs of the students several technical talks, workshops, personality development programs, soft skills and entrepreneurial activities are regularly conducted under professional societies besides curriculum.

About Warangal

The city of Warangal, located in the north east of Hyderabad in Telangana region of Andhra Pradesh, was accorded World Heritage status by UNESCO. Warangal was the capital of Kakatiya kingdom ruled by the Kakatiya dynasty from the 12th to the 14th centuries. The Kakatiya kings were great builders. Some of the famous examples of this architecture are the Thousand Pillared Temple, Ramappa Temple and Warangal Fort. Warangal is known for its beautiful lakes, temples and wildlife. It is rich in antiques and relics. The best season to visit Warangal is from September to February, when there is little humidity and average temperature is in the range of 22- 23°C.

SR Engineering College is located 15 km away from Warangal City on Warangal-Karimnagar Highway. Warangal is well connected by Rail and Road. The nearest Airport is in Hyderabad, which is 150 km away.

